

CLAIM AMENDMENTS

Please cancel claims 7, 9, and 25 without prejudice or disclaimer.

Please add new claims 29-30.

Please amend claims 1, 4, 8, 10, 23, and 26 as follows.

1. (Currently Amended) A system that includes

a first pulse train input to a switch having a repeating high value and low value at a first frequency, and a transition from the low value to the high value having a first edge sharpness;

a second pulse train input to the switch in synchronization with the first pulse train having a repeating high value and low value at a second frequency lower than the first frequency; and

a frequency changing circuit coupled between the input pulse train and the switch, the frequency changing circuit to one of multiply and divide the frequency of the second pulse train, wherein the output of the frequency changing circuit has substantially the frequency of the first pulse train,

the switch selecting an output from one of the first pulse train and the second pulse train to create an output pulse train appropriate to transition fault test an integrated circuit, depending upon a selection signal having a determined pulse train input to the switch;

the selection signal in synchronization with the first pulse train;

wherein the switch switches from the second pulse train to the first pulse train and substantially maintains the first edge sharpness of the first pulse train, and from the first pulse train to the second pulse train, during a low value of both the first and the second ~~pulse train~~ control signal.

2. (Original) The system as defined in claim 1 further including

a signal input to the switch having the substantially low value;

the switch selecting an output from one of the first pulse train input, the second pulse train input, and the third input, depending upon the selection signal;

wherein the switch switches from the second pulse train to the first pulse train by switching from the second pulse train to the signal to the first pulse train.

3. (Original) The system as defined in claim 1 further including
a signal input to the switch having substantially the low value;
the switch selecting an output from one of the first pulse train input, the second pulse train input, and the third input, depending upon the selection signal;

wherein the switch switches from the first pulse train to the second pulse train by switching from the first pulse train to the signal to the second pulse train.

4. (Currently Amended) The system as defined in claim 1 wherein the selection signal consists of one of:

a third pulse train ~~and a fourth pulse train~~; and
a ~~third~~ fourth pulse train.

5. (Original) The system as defined in claim 1 further including
an edge placement accuracy improvement circuit to improve the edge placement accuracy of an input pulse train;

wherein the output of the edge placement accuracy improvement circuit has substantially the edge placement accuracy of the first pulse train, and the edge placement accuracy improvement circuit is coupled between the input pulse train and the switch; and
the input pulse train.

6. (Original) The system as defined in claim 5 further including a pulse train alignment circuit to align the first pulse train with the second pulse train, wherein the pulse train alignment circuit is coupled to at least one of the input pulse train-first pulse train path and the second pulse train path.

7. (Canceled)

8. (Currently Amended) The system as defined in claim [[7]] 1 further including a pulse train alignment circuit coupled to at least one of the input pulse train-first pulse train path and the second pulse train path to align the first pulse train with the second pulse train.

9. (Canceled)

10. (Currently Amended) The system as defined in claim [[9]] 1 further including a pulse train alignment circuit coupled to at least one of the input pulse train-first pulse train path and the second pulse train path to align the first pulse train with the second pulse train.

11. (Original) The system as defined in claim 1 further including
a pulse train alignment circuit to align the selection signal with at least one of the second pulse train and the first pulse train.

12. (Original) The system as defined in claim 1 further including
a signal conditioning circuit to translate the voltage level of an output of the switch;
and
the output of the switch.

Claims 13-22 (Canceled).

23. (Currently Amended) A system that includes:
a first pulse train at a frequency adequate to load and unload at least one scan chain of an integrated circuit input to a switch;
a second pulse train in synchronization with the first pulse train having a frequency and pulse edge sharpness adequate to test the propagation time of an integrated circuit core at functional at-frequency rates input to the switch;
the switch that dynamically outputs alternatively a portion of the first pulse train or the second pulse train as an output, based on an input from an at least one selection signal;
and

the selection signal defined for the switch to output a signal to the ~~scan chains~~ switch to load and unload the scan chains and to transition fault test the integrated circuit core.

24. (Original) The system as defined in claim 23 further including:

a signal having a substantially low value input to the switch wherein the switch outputs alternatively the first pulse train portion or the second pulse train portion or the signal as an output, based on an input from the selection signal;

the switch switches the output from the second pulse train to the first pulse train by switching from the second pulse train to the signal to the first pulse train; and

the switch switches the output from the first pulse to the second pulse train by switching from the first pulse train to the signal to the second pulse train.

25. (Canceled)

26. (Currently Amended) The system as defined in claim ~~[[26]]~~ 23 further including

an alignment circuit coupled to at least one of the input pulse train-second pulse train path and the first pulse train path to align the first pulse train with the second pulse train.

27. (Original) The system as defined in claim 23 further including

a calibration circuit to align the selection signal with at least one of the first and second signal.

28. (Original) The system as defined in claim 23 wherein the selection signal is in synchronization with at least one of the first signal and the second signal.

29. (New) A system, comprising:

a first pulse train input to a switch having a repeating high value and low value at a first frequency, and a transition from the low value to the high value having a first edge sharpness;

a second pulse train input to the switch in synchronization with the first pulse train having a repeating high value and low value at a second frequency lower than the first frequency; and

a circuit is coupled between the input pulse train and the switch, the circuit having an edge placement accuracy improvement circuit and frequency changing circuit to both improve the edge placement accuracy of an input pulse train and one of multiply and divide the frequency of an input pulse train, wherein the output of the circuit has the edge placement accuracy and the frequency of the first pulse train,

the switch selecting an output from one of the first pulse train and the second pulse train to create an output pulse train appropriate to transition fault test an integrated circuit, depending upon a selection signal having a determined pulse train input to the switch;

the selection signal in synchronization with the first pulse train;

wherein the switch switches from the second pulse train to the first pulse train and substantially maintains the first edge sharpness of the first pulse train, and from the first pulse train to the second pulse train, during a low value of both the first and the second control signal.

30. (New) The system as defined in claim 29 further including a pulse train alignment circuit coupled to at least one of the input pulse train-first pulse train path and the second pulse train path to align the first pulse train with the second pulse train.

31. (New) The system as defined in claim 29 further including

a signal input to the switch having the substantially low value;

the switch selecting an output from one of the first pulse train input, the second pulse train input, and the third input, depending upon the selection signal;

wherein the switch switches from the second pulse train to the first pulse train by switching from the second pulse train to the signal to the first pulse train.